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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/989,817	11/19/2001	Matthew A. Pleis	CYPR-CD01191M	1792

7590 06/08/2004  
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EXAMINER  
ELAMIN, ABDELMONIEM I

ART UNIT PAPER NUMBER  
2116

DATE MAILED: 06/08/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	09/989,817	PLEIS ET AL.	
	<b>Examiner</b>	<b>Art Unit</b>	
	A Elamin	2116	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --**

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 19 November 2001.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-21 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-21 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |   |   |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)             | 4) <input type="checkbox"/> Interview Summary (PTO-413)                     |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)    | Paper No(s)/Mail Date. _____  |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____   | 6) <input type="checkbox"/> Other: _____                                    |

**DETAILED ACTION**

***Claim Rejections - 35 USC § 102***

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1-3 are rejected under 35 U.S.C. 102(e) as being anticipated by Snyder et al, US. Pat. No. 6,724,220.

3. Claims 1, , Snyder teaches a circuit [*title, abstract*], comprising;

a bus for communicating data; a microprocessor for processing data, said microprocessor coupled to said bus [*Fig. 1, col. 1, lines 21-22*];

a programmable functional component coupled to said bus [*Fig. 1*], wherein said programmable functional component includes a plurality of functional blocks programmable to provide a plurality of functions and configurations [*Fig. 1, col. 5, lines 5-9*]; and

a memory for storing data including information associated with said functions and configurations, said memory coupled to said bus [*col. 2, lines 41-45, col. 3, lines 11-14*].

4. Claim 2, Snyder teaches the programmable functional component includes;

a programmable interconnect for coupling components to said bus  
[*programmable interconnect 123 of Fig. 1*];

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an analog functional block configurable to perform analog functions, said analog block coupled to said interconnect [*analog block 121 of Fig. 1*]; and

a digital functional block configurable to perform digital functions, said digital block coupled to said interconnect [*digital block 122 of Fig. 1*].

5. Claim 3, Snyder teaches a programmably configurable external communication port for communicatively coupling with external devices [*Fig. 1, col. 5, lines 48-59*].

6. **Claims 10-21 are rejected under 35 U.S.C. 102(e) as being anticipated by Hanna, US. Pat. No. 6,542,844.**

7. Claims 10, 14, 17 and 21, Hanna teaches an electronic device dynamic configuration method [*title, abstract, col. 6, lines 25-51*], comprising:

loading a plurality of configuration images a memory of said electronic device [*col. 6, lines 37-41*];

configuring said electronic device in accordance with a first configuration image [*col. 3, lines 26-28*];

performing functions in accordance with said first configuration image [*col. 3, lines 26-43*];

reconfiguring said electronic device in accordance with a second configuration image [*col. 3, lines 26-28*]; and

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executing functions in accordance with said second configuration image [*col. 3, lines 26-43*].

8. Claim *11 and 19*, Hanna teaches reconfiguration of said electronic device in accordance with a second configuration image is performed in response to a predetermined condition or event [*col. 3, lines 16-43*].

9. Claims *12 and 20*, Hanna teaches the electronic device is a programmable IC [*col. 1, lines 15-20*].

10. Claims *13 and 15-16*, Hanna teaches the first and the second configuration images define different functions and ... [*abstract, col. 1, line 60 thru col. 2, line 11*].

11. Claims *18*, Hanna teaches downloading said first configuration image into a memory space of said electronic device; and downloading said second configuration image into another memory space of said electronic device [*Fig. 4, col. 3, line 59 thru col. 4, line 8, col. 6, lines 37-41*].

### ***Claim Rejections - 35 USC § 103***

12. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

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13. Claims 4-9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Snyder et al, US. Pat. No. 6,724,220 in view of Hanna, US. Pat. No. 6,542,844.

14. Claims 4-9, Snyder fails to teach said memory stores a plurality of configuration images that define the configuration and functionality of said circuit.

Hanna teaches a system for reconfiguring a programmable device comprising a storage for storing a plurality configuration images that define the configuration and functionality of the programmable device [*title, abstract, col. 3, line 25 thru col. 4, line 8, col. 6, lines 27-52*].

It would have been obvious to one of ordinary skill in the art at the time the invention was made to modify Snyder to include said memory stores a plurality of configuration images that define the configuration and functionality of said circuit, because it allows reconfiguration of programmable devices via download of binary information [*see Hanna, col. 1, lines 15-20*].

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to A Elamin whose telephone number is (703)305-3804. The examiner can normally be reached on MON-FRI 9:30 AM - 6:00 PM.


If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne Browne can be reached on (703) 308-1159. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

A Elamin  
Primary Examiner  
Art Unit 2116

*June 2, 2004*



**A. ELAMIN**  
**PRIMARY EXAMINER**